

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	5012	375/376	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L3	100	(adaptive or (dynamically near (set or setting))) with (frequency adj divid\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L4	204	375/215	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L5	379	(clock with extract\$5) and (phase with comparat\$5) and (frequency adj divided) and (vco or (voltage adj control\$3)) and (PLL or (phase adj locked)) and (average or LPF or low adj pass)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L6	0	L5 and L4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L7	4	(regeneration same control) and threshold and (clock same phase same setting) and decision and (eye same (pattern or diagam))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L8	3426	375/371	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L9	31	optimal with point with (eye same (diagram or pattern))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05

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L10	3	regeneration with threshold with clock with phase with decision	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L11	0	(clock with extract\$5) with (phase with comparat\$5) with (frequency adj divided) with (vco or (voltage adj control\$3)) with (PLL or (phase adj locked)) with (average or LPF or low adj pass)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L12	1	(regeneration with control) with threshold with (clock with phase with setting) with decision with (eye with (pattern or diagam))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L13	1251	331/34	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L14	4	regeneration with control with (eye same (diagram or pattern))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L15	3	clock with extraction with transmission with rate with phase with synchroniz\$5 and regeneration	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L16	354	375/214	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L17	11	((frequency adj divid\$3) with (set or setting) with dynamically)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05

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L18	2	clock same extracti\$3 same (transmi\$5 same rate) same (phase same synchroniz\$5) same regeneration same eye	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L19	7	phase with compar\$5 with "exclusive-or" with rising	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L20	1	L5 and L16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L21	7	("4114710" "4164758" "4357943" "4663844" "5394107" "5506531" "5635875").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/17 12:05
L22	4	phase with compar\$5 with "or-exclusive"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L23	0	phase with compar\$5 with exclusive	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L24	1	"09/997655"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L25	8	BER with measurement with cycle with signal	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L26	4254	331/25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05

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L27	24	clock and extracti\$3 and (transmi\$5 same rate) and (phase same synchroniz\$5) and regeneration and (eye same (diagram or pattern))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L28	0	(PLL or (phase adj locked adj loop)) and ((frequency adj divider) with (set or setting) with dinamically)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L29	28	(adaptiv\$3 or (dynamically near (set or setting))) with (frequency adj divid\$3) and (phase with difference)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L30	6	(PLL or (phase adj locked adj loop)) and ((frequency adj divid\$3) with (set or setting) with dynamically)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L31	32	("4097697" "4799790" "4823360" "5418789").PN. OR ("5896391").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/17 12:05
L32	539	331/12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L33	1	L5 and L32	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L34	1	(regeneration with control) same threshold same (clock with phase with setting) same decision same (eye with (pattern or diagram))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L35	8	(regeneration same control) and threshold and (clock same phase) and (decision and hold\$4) and (eye same (pattern or diagram)) and exclusive	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05

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L36	78	388/911	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L37	8	(clock with extract\$5) same (phase with comparat\$5) same (frequency adj divided) same (vco or (voltage adj control\$3)) same (PLL or (phase adj locked)) same (average or LPF or low adj pass)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L38	2	clock same extracti\$3 with (transmi\$5 with rate) same (phase with synchroniz\$5) and regeneration and eye	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L39	2	"5896391".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L40	3	((regeneration with circuit) and (clock with extraction) and (voltage with threshold)).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L41	16	((regeneration with circuit) and (clock with extraction)).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L42	10	((adaptive or dinamically) near12 frequency adj divid\$3) and (phase with difference)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L43	6	("5696792" "5920271" "5983082").PN. OR ("6040738").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/17 12:05

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L44	6	regeneration and (eye same (diagram or pattern)) and (adjacent with point with (match or equal or "same"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L45	6	("5696792" "5920271" "5983082").PN. OR ("6040738").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/17 12:05
L46	5012	375/376	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L47	38	L5 and L46	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L48	0	phase with comparat\$5 with (frequency adj divided) with (vco or (voltage adj control\$3)) with (PLL or (phase adj locked)) with (clock with extraction)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L49	14	clock with extraction with transmission with rate with phase	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L50	1473	375/362	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L51	10	L5 and L50	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L52	2	"2002141367".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05

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L53	25	L5 and L8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L54	4379	375/354	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L55	16	L5 and L54	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L56	2	"4516083".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L57	1	regeneration with (eye same (diagram or pattern)) and (optimal adj point)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L58	110	(adaptiv\$3 or (dynamically near (set or setting))) with (frequency adj divid\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L59	37	regeneration and (eye same (diagram or pattern)) and (adjacent with point)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L60	4	"4114710".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05

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L61	38	L5 and L2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L62	430	BER with measurement with rate with signal	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L63	2	"4625180".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L64	375	phase with comparat\$5 with (frequency adj divided) with (vco or (voltage adj control\$3)) with (PLL or (phase adj locked))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L65	0	phase with compar\$5 with "or-exclusive"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L66	768	331/4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L67	1	L5 and L66	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L68	2287	phase with comparat\$5 with (frequency adj divided)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05

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L69	2	"20010038674".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L70	5	phase with comparat\$5 with (frequency adj divided) with (vco or (voltage adj control\$3)) with (PLL or (phase adj locked)) with average	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L71	2202	455/260	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L72	4	L5 and L71	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L73	24	((PLL or (phase adj locked adj loop)) and ((frequency adj divider) with adaptive)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L74	27	adaptive near5 (frequency adj divid\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L75	28	("5896391").URPN.	USPAT	OR	ON	2007/12/17 12:05
L76	2	((adaptive or dinamicly) near2 frequency adj divid\$3) and (phase with difference)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L77	13	("4595992").PN. OR ("5384552").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/17 12:05

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L78	0	(PLL or (phase adj locked adj loop)) and ((frequency adj divid\$3) with (set or setting) with dinamically)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L79	430	kim.in. and "spread" and (spread adj spectrum)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L80	15	BER with measurement with period with signal	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L81	2	(adaptive adj frequency adj divid\$3) and (phase with difference)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L82	81	clock with extraction with transmission with rate	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L83	0	L82 and L64	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L84	25	L5 and L26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L85	2208	(PLL or (phase adj locked adj loop)) and ((frequency adj divider) with (set or setting))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05

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L86	0	((regeneration with circuit) and (clock wit extraction)).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L87	0	regeneration and (eye same (diagram or pattern)) and (adjacent with poin)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L88	8	regeneration and (eye same (diagram or pattern)) same (adjacent with point)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L89	353	phase with compar\$5 with "exclusive-or"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L90	24	(regeneration same control) and threshold and (clock same phase) and decision and (eye same (pattern or diagam))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L91	1	"09/111772"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L92	1	(regeneration same control) and threshold and (clock same phase) and (decision with hold) and (eye same (pattern or diagam))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L93	51	regeneration with (eye same (diagram or pattern))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L94	32	("4097697" "4799790" "4823360" "5418789").PN. OR ("5896391").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/17 12:05

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L95	3	(PLL or (phase adj locked adj loop)) and ((frequency adj divider) with (set or setting) with dynamically)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L96	27	((sweeping or sampling) and (time or interval)) and (voltage with phase) and (error adj rate) and (cycle adj period)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L97	7	power with off with pll with frequency with dividing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L98	4	L5 and L13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L99	1	((sweeping or sampling) with (time or interval)) with (voltage with phase) with (error adj rate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L100	32	("4097697" "4799790" "4823360" "5418789").PN. OR ("5896391").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/17 12:05
L101	2	"5774023".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L102	108	(adaptive or dynamical\$2) with (frequency adj divided)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L103	0	L5 and L36	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05

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L104	10	phase with compar\$5 with "exclusive-or" with duty	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L105	990	(PLL or (phase adj locked adj loop)) and ((frequency adj divider) with change)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L106	1	("4595992").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/17 12:05
L107	288	455/180.3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L108	3	"11170392"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L109	5	o/e with filter\$4 with equaliz\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L110	9	(regeneration same control) and threshold and (clock same phase) and (decision and hold\$4) and (eye same (pattern or diagam))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L111	2028	375/373	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L112	195	(adaptive or dynamical\$2) with (frequency adj divid\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05

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L113	0	regeneration with (eye same (diagram or pattern)) and (optimal adj poin)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L114	3	clock with extraction with transmission with rate with phase with synchroniz\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L115	2	(clock with extract\$5) with (phase with comparat\$5) with (frequency adj divided) with (vco or (voltage adj control\$3)) with (PLL or (phase adj locked))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L116	0	kim.in. and "spread" and spread adj sprectrum	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L117	12	L5 and L111	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L118	0	((frequency adj divid\$3) with (set or setting) with dinamically)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L119	55	adaptive near10 (frequency adj divid\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L120	18	(clock with extract\$5) and (phase with comparat\$5) and (setting with frequency adj divided) and (vco or (voltage adj control\$3)) and (PLL or (phase adj locked)) and (average or LPF or low adj pass)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05

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L121	0	L5 and L107	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L122	1	(regeneration with control) and threshold and (clock with phase with setting) and decision and (eye with (pattern or diagram))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L123	103	(adaptive or dynamical\$2) with (frequency adj divid\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L124	0	((sweeping or sampling) and (time or interval)) and (voltage with phase) and (error adj rate) and (cycle adj period)).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L125	0	((sweeping or sampling) with (time or interval)) with (voltage with phase) with (error adj rate) with (cycle adj period)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L126	379	(clock with extract\$5) and (phase with comparat\$5) and (frequency adj divided) and (vco or (voltage adj control\$3)) and (PLL or (phase adj locked)) and (average or LPF or low adj pass)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L127	4	("4097697" "4799790" "4823360" "5418789").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/17 12:05
L128	2	"20020141367".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05

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L129	19	BER with cycle near signal	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:21
L130	430	BER with cycle with signal	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:05
L131	2	"5323420".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:25
L132	3	"3384711".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:25
L133	4	"4209852".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/17 12:25

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A circuit to detect the crossing of at least one **voltage threshold** by an input voltage A **regeneration circuit** (16) is disclosed for regenerating noisy, ...
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... by comparing the amplitude of the input signal with a **voltage threshold**. ... supplied by the **clock** CU which comprises: (1) the **regeneration circuit** ...
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Monitor for an electrically-controlled system

... Circuit for protecting storage cells; **Clock regeneration circuit** ... representative of a level of said voltage signal relative a **voltage threshold**. ...
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... High-withstand-voltage integrated circuit; **Clock regeneration circuit** the reference voltage node to provide a **voltage threshold** when the output is ...
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Circuitry for regenerating digital signals in extended distance ...

Abstract, A **regeneration circuit** (16) is disclosed for regenerating noisy, and further having a HIGH **voltage threshold** and a LOW **voltage threshold**, ...
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- ☐ 1. Circuitry for regenerating digital signals in extended distance communications systems
Asprey, Robert R. (Cybex Corporation), UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Jun 1994
patno:US5323420
...monitor. A multiplexing device, represented by switches 56 and 58, multiplexes **clock** and data signals from mouse 42 and keyboard 44 and applies the multiplexed signals to **clock** and data conductors 60 and 62, respectively, of cable 48. While signal conditioning...
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- ☐ 2. Repeater for pulse code modulated signals
BOXALL FRANK S, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, May 1968
patno:US3384711
...known in the art. **Clock** generation circuit...specifically to the **clock regeneration circuit** 15, the pulse code...are coupled to the **clock** generation circuit...controlled by the **voltage threshold** control circuit 17...
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- ☐ 3. Signal processing and memory arrangement
Hyatt, Gilbert P., UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Jun 1980
patno:US4209852
...CCD 920. Mode logic 921 may then generate **clock** pulses 922 to shift the stored charge through...to digital form as digital signals 927. **Clock** signal 922 and convert signal 923 may be...Ser. No. 7490 and decoder 7442. Gating of **clock** signals and generation of quadrature signals...
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☐ 1. Electrical energy source

Pierce, Gerald, *UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION*, Jan 2007

patno:US20070019453

...rated at 2 Watts. Resistor R3 is 45Ω and rated at 2 Watts. **Clock** crystal is approximately 200 Mhz. The inverter shown may...such as, e.g., when a predetermined low inverter output **voltage threshold** has been reached), the regeneration device 625 can feed battery...

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☐ 2. Circuitry for regenerating digital signals in extended distance communications systems

Asprey, Robert R. (Cybex Corporation), *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Jun 1994

patno:US5323420

...monitor. A multiplexing device, represented by switches 56 and 58, multiplexes **clock** and data signals from mouse 42 and keyboard 44 and applies the multiplexed signals to **clock** and data conductors 60 and 62, respectively, of cable 48. While signal conditioning...

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☐ 3. Repeater for pulse code modulated signals

BOXALL FRANK S, *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, May 1968

patno:US3384711

...known in the art. **Clock** generation circuit...specifically to the **clock regeneration circuit** 15, the pulse code...are coupled to the **clock** generation circuit...controlled by the **voltage threshold** control circuit 17...

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☐ 4. Signal processing and memory arrangement

Hyatt, Gilbert P., *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Jun 1980

patno:US4209852

...CCD 920. Mode logic 921 may then generate **clock** pulses 922 to shift the stored charge through...to digital form as digital signals 927. **Clock** signal 922 and convert

signal 923 may be...Ser. No. 7490 and decoder 7442. Gating of **clock** signals and generation of quadrature signals...

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- ☐ 1. Electrical energy source
Pierce, Gerald, *UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION*, Jan 2007
patno:US20070019453
...control circuit 608. [0073] When directed by the control circuit 608 (such as, e.g., when a predetermined low inverter output **voltage threshold** has been reached), the regeneration device 625 can feed battery charging current to the battery pack 601 via timer 607...
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- ☐ 2. Circuitry for regenerating digital signals in extended distance communications systems
Asprey, Robert R. (Cybex Corporation), *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Jun 1994
patno:US5323420
A regeneration circuit (16) is disclosed for regenerating noisy, received digital signals in a high impedance signal line (22). A buffer amplifier (30) is provided with a feedback resistor (36), with the feedback resistor and input to the buffer amplifier ...
Full text available at patent office. For more in-depth searching go to  LexisNexis[®]
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- ☐ 3. Temperature-compensated apparatus for monitoring current having controlled sensitivity to supply voltage
Brokaw, A. Paul (Analog Devices, Inc.), *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Mar 1992
patno:US5095274
...determine whether the voltage sensed is above or below a preset **voltage threshold**. The threshold voltage is supplied by a circuit drawn functionally...across the shunt resistance 46 with a predefined switching **voltage threshold**. Input terminals 48 and 50 are connected to the lamp monitoring...
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- ☐ 4. Temperature-compensated apparatus for monitoring current having controlled sensitivity to supply voltage
Brokaw, A. Paul (Analog Devices, Inc.), *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, May 1994
patno:US5313165

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...determine whether the voltage sensed is above or below a preset **voltage threshold**. The threshold voltage is supplied by a circuit drawn functionally...across the shunt resistance 46 with a predefined switching **voltage threshold**. Input terminals 48 and 50 are connected to the lamp monitoring...

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☐ 5. TEMPERATURE-COMPENSATED APPARATUS FOR MONITORING CURRENT HAVING REDUCED SENSITIVITY TO SUPPLY VOLTAGE

BROKAW, A., Paul (ANALOG DEVICES, INC.), PATENT COOPERATION TREATY APPLICATION, Apr 1991
patno:WO9104495

...sensed is above or below a preset **voltage threshold**. The threshold voltage is supplied...resistance 46 with a predefined switching **voltage threshold**. Input terminals 48 and 50 are connected...connected to terminal 56. The switching **voltage threshold** for the comparator is generated as...

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☐ 6. TEMPERATURE-COMPENSATED APPARATUS FOR MONITORING CURRENT HAVING REDUCED SENSITIVITY TO SUPPLY VOLTAGE

BROKAW, A., Paul (ANALOG DEVICES, INCORPORATED), EUROPEAN PATENT, Dec 1993
patno:EP573408


...determine whether the voltage sensed is above or below a preset **voltage threshold**. The threshold voltage is supplied by a circuit drawn functionally...across the shunt resistance 46 with a predefined switching **voltage threshold**. Input terminals 48 and 50 are connected to the lamp monitoring...

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☐ 7. Repeater for pulse code modulated signals

BOXALL FRANK S, UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, May 1968
patno:US3384711


...now more specifically to the clock **regeneration circuit** 15, the pulse code type pulses appearing...transformer 13 which is controlled by the **voltage threshold** control circuit 17. This voltage...6 of transformer 13, on which the **voltage threshold** signal appears, is coupled to the...

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☐ 8. Signal processing and memory arrangement

Hyatt, Gilbert P., UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT, Jun 1980
patno:US4209852

An improved arrangement is provided for processing analog and digital signals, where particular advantages are obtained using charge coupled devices (CCDs). A memory arrangement utilizes a novel refresh circuit to re-establish signal amplitudes which are ...

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[Proceedings of the IRE](#)
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Nizhizawa, H.; Yamada, Y.; Shibata, Y.; Habara, K.;
[Photonics Technology Letters, IEEE](#)
Volume 11, [Issue 6](#), June 1999 Page(s):733 - 735
Digital Object Identifier 10.1109/68.766801
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- ☐ 3. **Complementary-SCR ESD protection circuit with interdigitated finger-tyl submicron CMOS IC's**
Ming-Dou Ker; Chung-Yu Wu;
[Electron Devices, IEEE Transactions on](#)
Volume 42, [Issue 7](#), July 1995 Page(s):1297 - 1304
Digital Object Identifier 10.1109/16.391212
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- ☐ 4. **Distinct regeneration capability of 40 Gbit/s signal impaired with amplitu using SOA-based all-optical polarization discriminated switch**
Tsurusawa, M.; Nishimura, K.; Inohara, R.; Usami, M.;
[Optical Fiber Communication Conference and Exhibit, 2002. OFC 2002](#)
17-22 Mar 2002 Page(s):84 - 86
Digital Object Identifier 10.1109/OFC.2002.1036221
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Chang, J.W.; Gorur, R.S.;
[Properties and Applications of Dielectric Materials, 1994. Proceedings of the Conference on](#)
Volume 1, 3-8 July 1994 Page(s):266 - 269 vol.1
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